

IPW

PTO/SB/21 (04-04)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/711,137
	Filing Date	August 27, 2004
	First Named Inventor	Crosland, Andrew
	Art Unit	Unassigned
	Examiner Name	Unassigned
Total Number of Pages in This Submission	Attorney Docket Number	015114-053510US

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Return Postcard PTO Form SB/08A and SB/08B
Remarks		The Commissioner is authorized to charge any additional fees to Deposit Account 20-1430.
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Townsend and Townsend and Crew LLP Melvin D. Chan Reg. No. 39,626	
Signature		
Date	10-27-04	

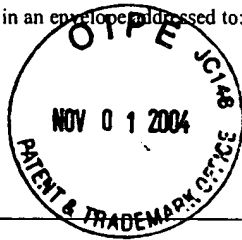
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10/29/04



PATENT
Attorney Docket No.: 15114-053510US
Client Reference No.: A618

TOWNSEND and TOWNSEND and CREW LLP

By:


Tiffany Wu

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

ANDREW CROSLAND et al.

Application No.: 10/711,137

Filed: August 27, 2004

For: EMBEDDED PROCESSOR WITH
WATCHDOG TIMER FOR
PROGRAMMABLE LOGIC

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

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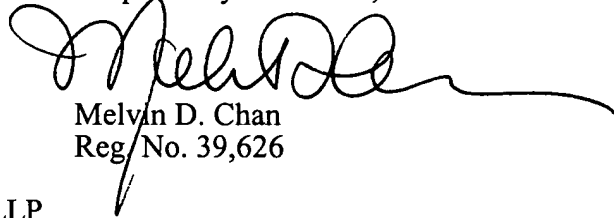
Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 CFR §1.98(d), copies of the references can be found in Application No. 09/880,734, filed June 12, 2001 (Attorney Docket No. 015114-053500US). It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



Melvin D. Chan
Reg/No. 39,626

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60329572 v1



PTO/SB/08A (08-03)

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known			
		Application Number	10/711,137		
		Filing Date	August 27, 2004		
		First Named Inventor	Crosland, Andrew		
		Art Unit	Unassigned		
		Examiner Name	Unassigned		
Sheet	1	of	3	Attorney Docket Number	015114-053510US

U.S. PATENT DOCUMENTS ⁺					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
	AA	US-4,488,259	12-11-1984	Mercy	
	AB	US-4,617,479	10-14-1986	Hartmann et al.	
	AC	US-4,710,927	12-01-1987	Miller	
	AD	US-4,871,930	10-03-1989	Wong et al.	
	AE	US-5,241,224	08-31-1993	Pedersen et al.	
	AF	US-5,258,668	11-02-1993	Cliff et al.	
	AG	US-5,260,610	11-09-1993	Pedersen et al.	
	AH	US-5,260,611	11-09-1993	Cliff et al.	
	AI	US-5,412,260	05-02-1995	Tsui et al.	
	AJ	US-5,436,575	07-25-1995	Pedersen et al.	
	AK	US-5,550,782	08-27-1996	Cliff et al.	
	AL	US-5,790,479	08-04-1998	Conn	
	AM	US-6,097,211	08-01-2000	Couts-Martin et al.	
	AN	US-6,233,205	05-15-2001	Wells et al.	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
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60329572 v1

Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/711,137
				Filing Date	August 27, 2004
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				Examiner Name	Unassigned
Sheet	2	of	3	Attorney Docket Number	015114-053510US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AO	AITKEN, R.C., and AGARWAL, V.K., "A Diagnosis Method Using Pseudo-Random Vectors Without Intermediate Signatures," Proc. of Int. Conf. on Computer-Aided Design (ICCAD), IEEE pp. 574-577 (1989).	
	AP	Altera "APEX 20K Programmable Logic Device Family Data Sheet," May 1999, 7 pages total.	
	AQ	Altera "FLEX 10K Embedded Programmable Logic Family Data Sheet," May 1999, 7 pages total.	
	AR	Altera "FLEX 8000 Programmable Logic Device Family Data Sheet," May 1999, 5 pages total.	
	AS	Altera "IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices," August 1999, Application Note 39, 29 pages total.	
	AT	Altera "MAX 7000 Programmable Logic Device Family Data Sheet," May 1999, 6 pages total.	
	AU	"AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages.	
	AV	"CS2000 Reconfigurable Communications Processor Family Product Brief," Advance Product Information from ChameleonSystems, Inc., 2000, pages 1-8	
	AW	DeHON, DPGA-Coupled Microprocessors: Commodity ICs for the Early 21st Century, Artificial Intelligence Laboratory, Massachusetts Institute of Technology, Cambridge, MA, IEEE, February, 1994, pp. 31-33	
	AX	GHOSH-DASTIDAR, J., and TOUBA, N.A., "A Rapid and Scalable Diagnosis Scheme for BIST Environments With a Large Number of Scan Chains," Proc. of IEEE VLSI Test Symposium, pp. 79-85 (2000).	
	AY	GHOSH-DASTIDAR, J., DAS, D., and TOUBA, N.A., "Fault Diagnosis in Scan-Based BIST using Both Time and Space Information," Proc. of International Test Conf., IEEE, pp. 95-102 (1999).	
	AZ	HAUSER, and WAWRZYNEK, University of California, Berkeley, "Garp: A MIPS Processor with a Reconfigurable Coprocessor," IEEE, April 1997, pp. 12-21.	
	BA	IBM Corporation, "Mixture of Field and Factory Programmed Logic Cells in a Single Device," IBM Technical Disclosure Bulletin, April 1995, pp. 499-500	
	BB	MC ANNEY, M.G. and SAVIR, J., "There is Information in Faulty Signatures," Proc. of International Test Conf., IEEE, pp. 630-636 (1987).	
	BC	"Motorola Technical Developments," Magazine of Motorola, Inc., Vol. 39, September 1999, pp. i-vii and 77-80.	
	BD	NAGEL, "ACM Computing Surveys: Synergy Between Software and Hardware," Carnegie Mellon University, Department of Electrical Engineering and Computer Science, Pittsburgh, PA, December, 1996, pp. 1-3.	

Examiner Signature		Date Considered	
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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Sheet	3	of	3	Attorney Docket Number	015114-053510US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	BE	RAZDAN, and SMITH, "A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Harvard University, Cambridge, MA, Digital Equipment Corporation, Hudson, MA, November, 1994, pp. 172-180.	
	BF	"Triscend E5 Configurable System-on-Chip Family," Product Description from Triscend Corporation, January, 2000 (Version 1.00), pp. i-ii and 1-90.	
	BG	"Wireless Base Station Design Using Reconfigurable Communications Processors," Wireless Base Station White paper from ChameleonSystems, Inc., 2000, pages 1-8.	
	BH	WITTIG, and CHOW, "OneChip" An FPGA Processor With Reconfigurable Logic," Department of Electrical and Computer Engineering, University of Toronto, Ontario, Canada, IEEE, September, 1996, pp. 126-135.	
	BI	WYNN, "In-Circuit Emulation for ASIC-Based Designs," Xilinx, Inc., VLSI Systems Design, October, 1986, pp. 38-39, and 42-45.	

Examiner Signature		Date Considered	
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